

FIG. 5

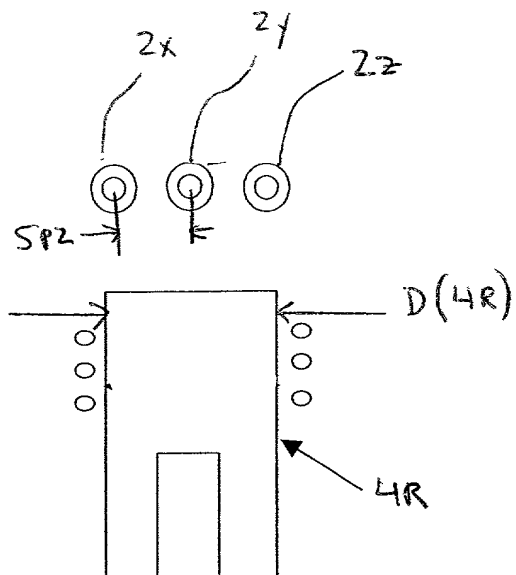


FIG. 6A

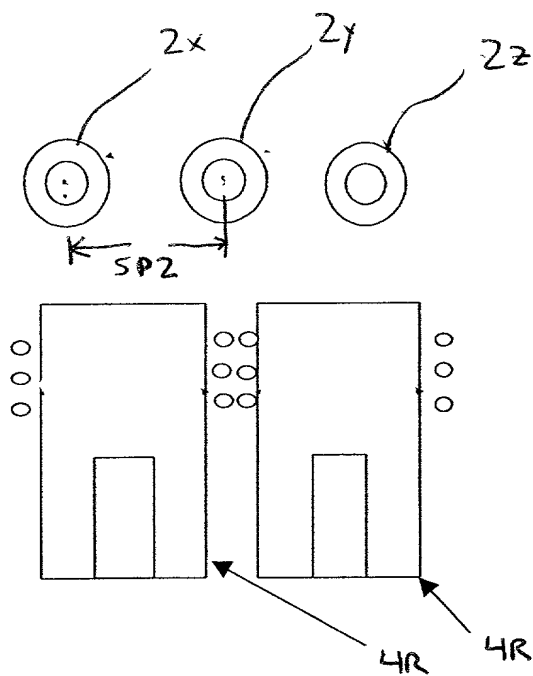


FIG. 6B



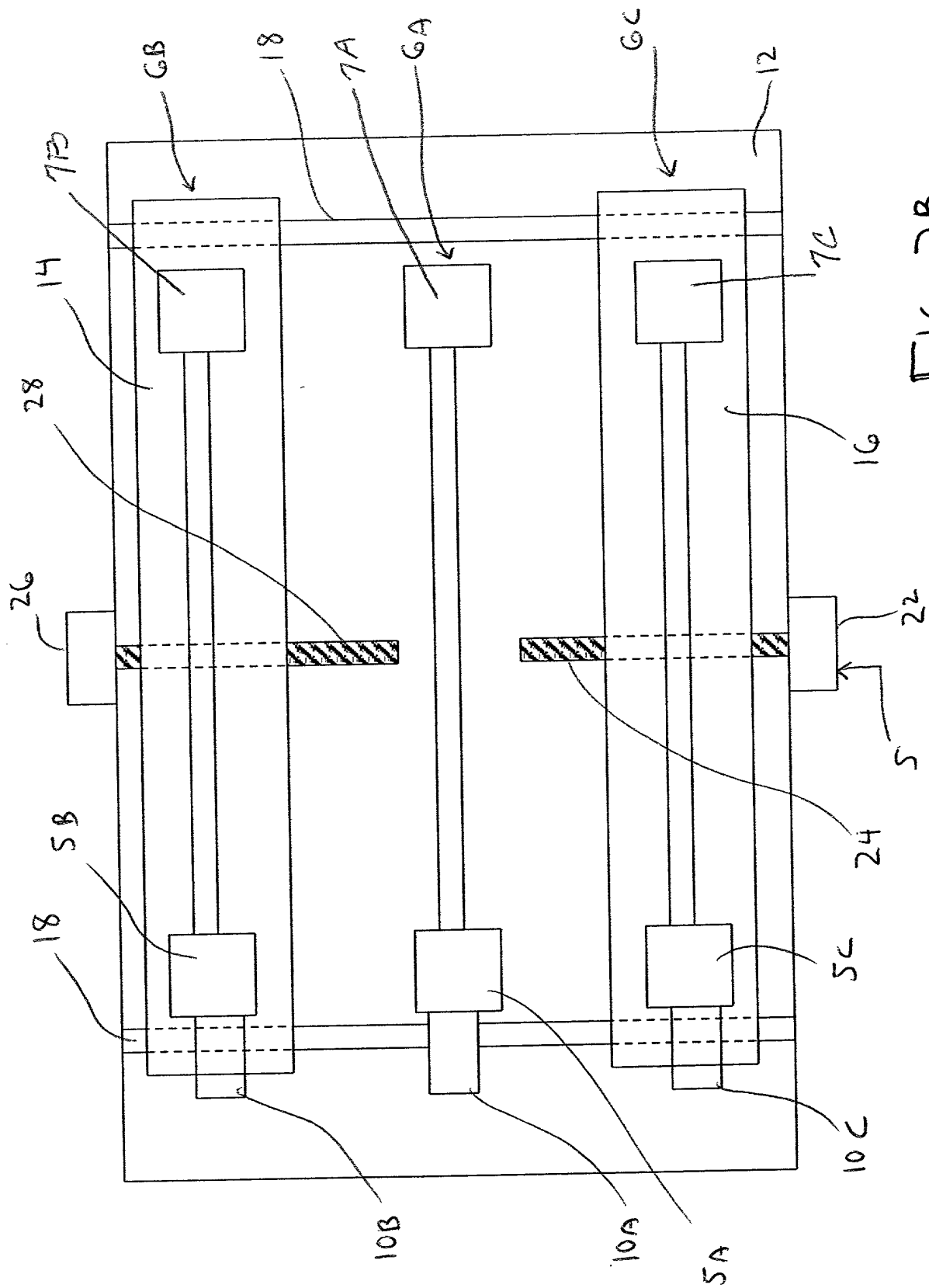


FIG. 7B

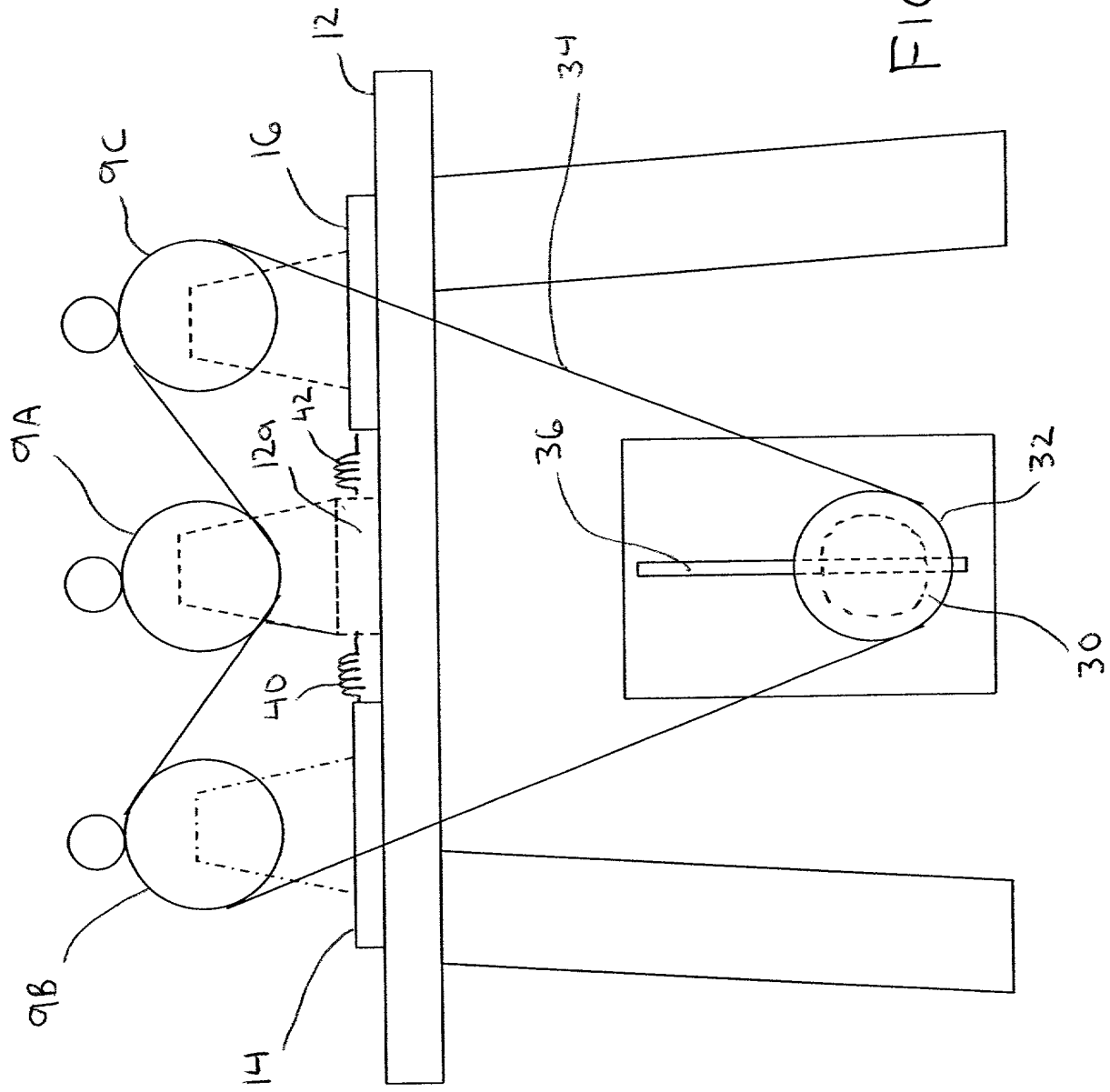


FIG. 8A

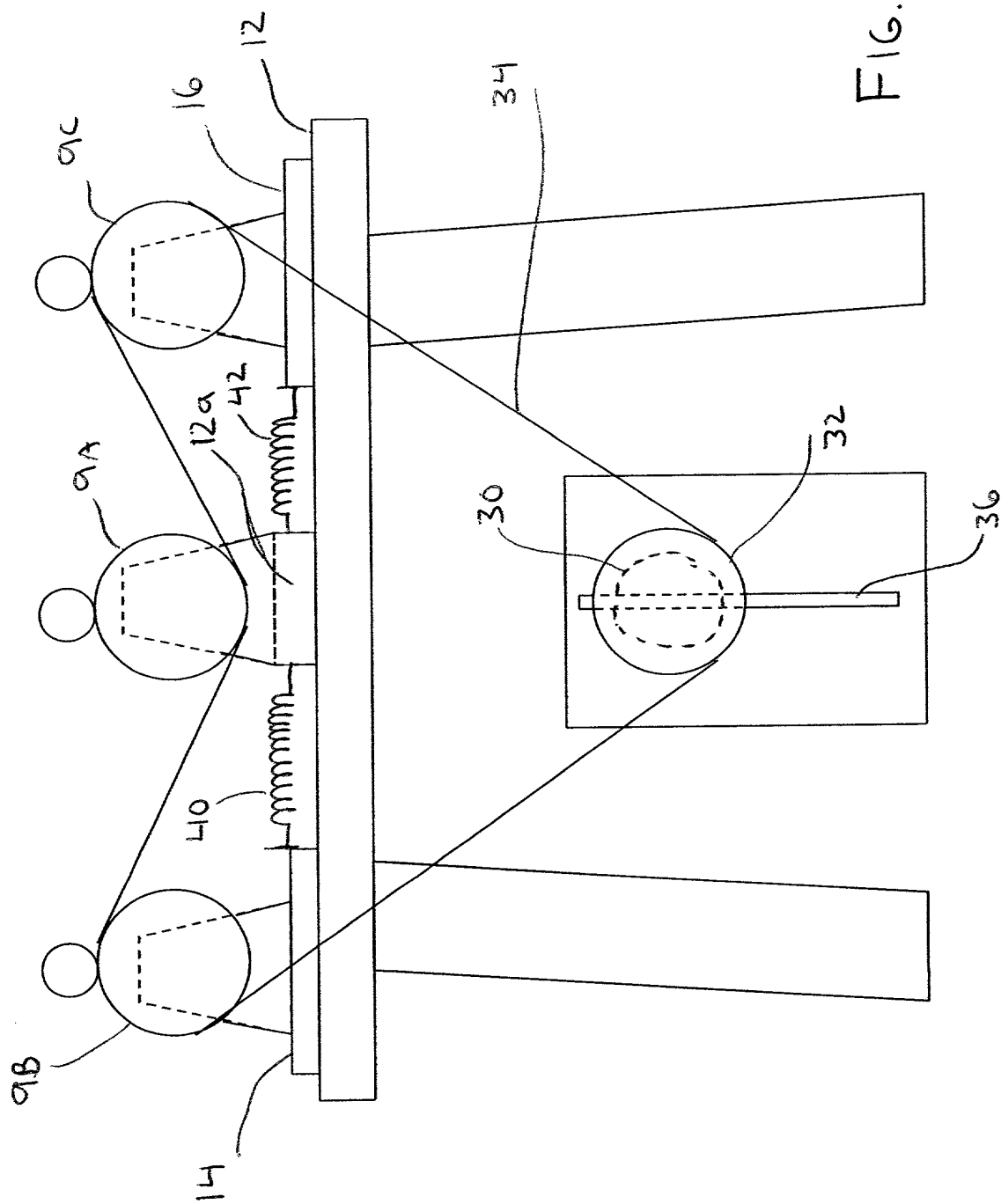


FIG. 8B



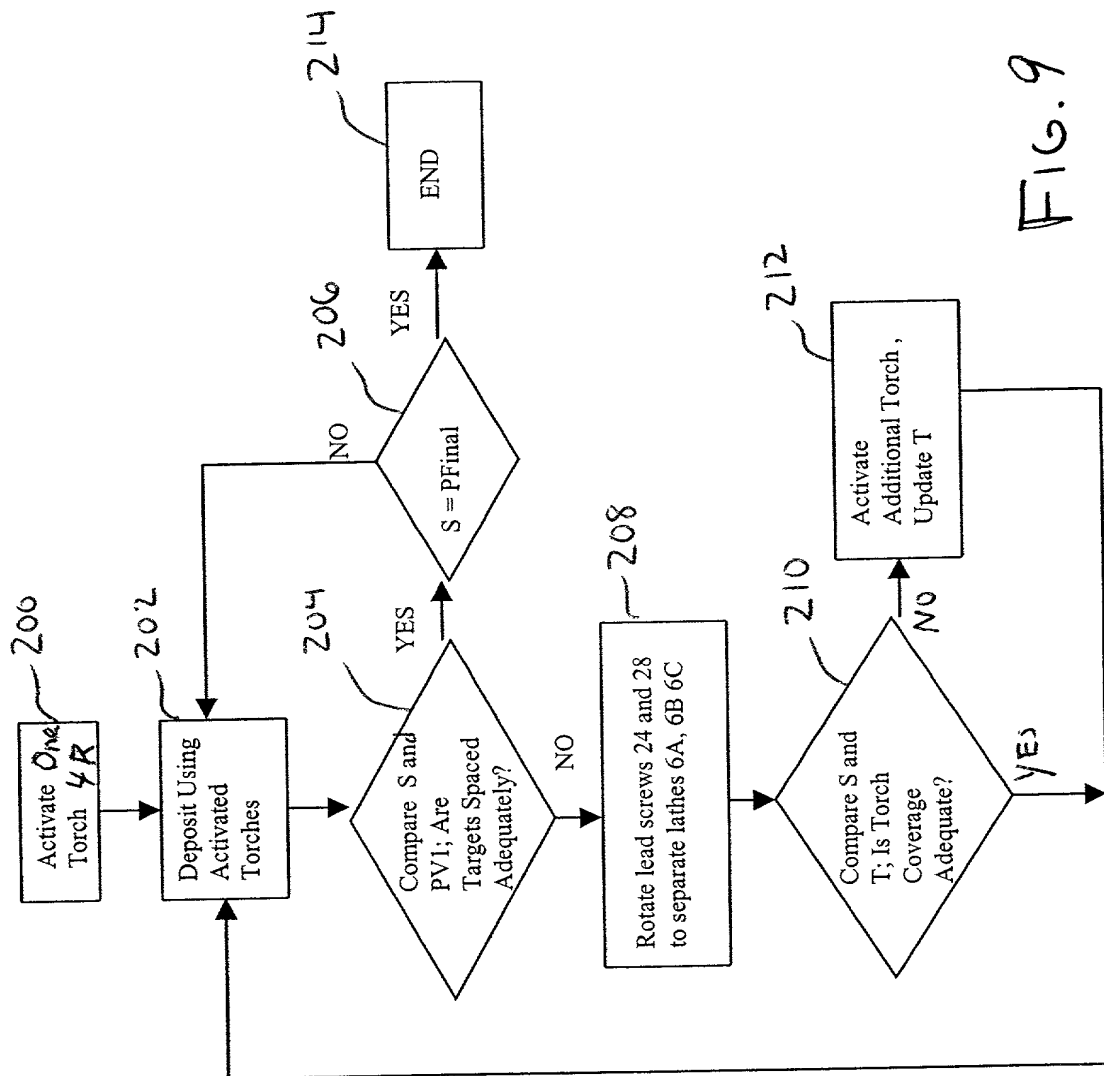


FIG. 9

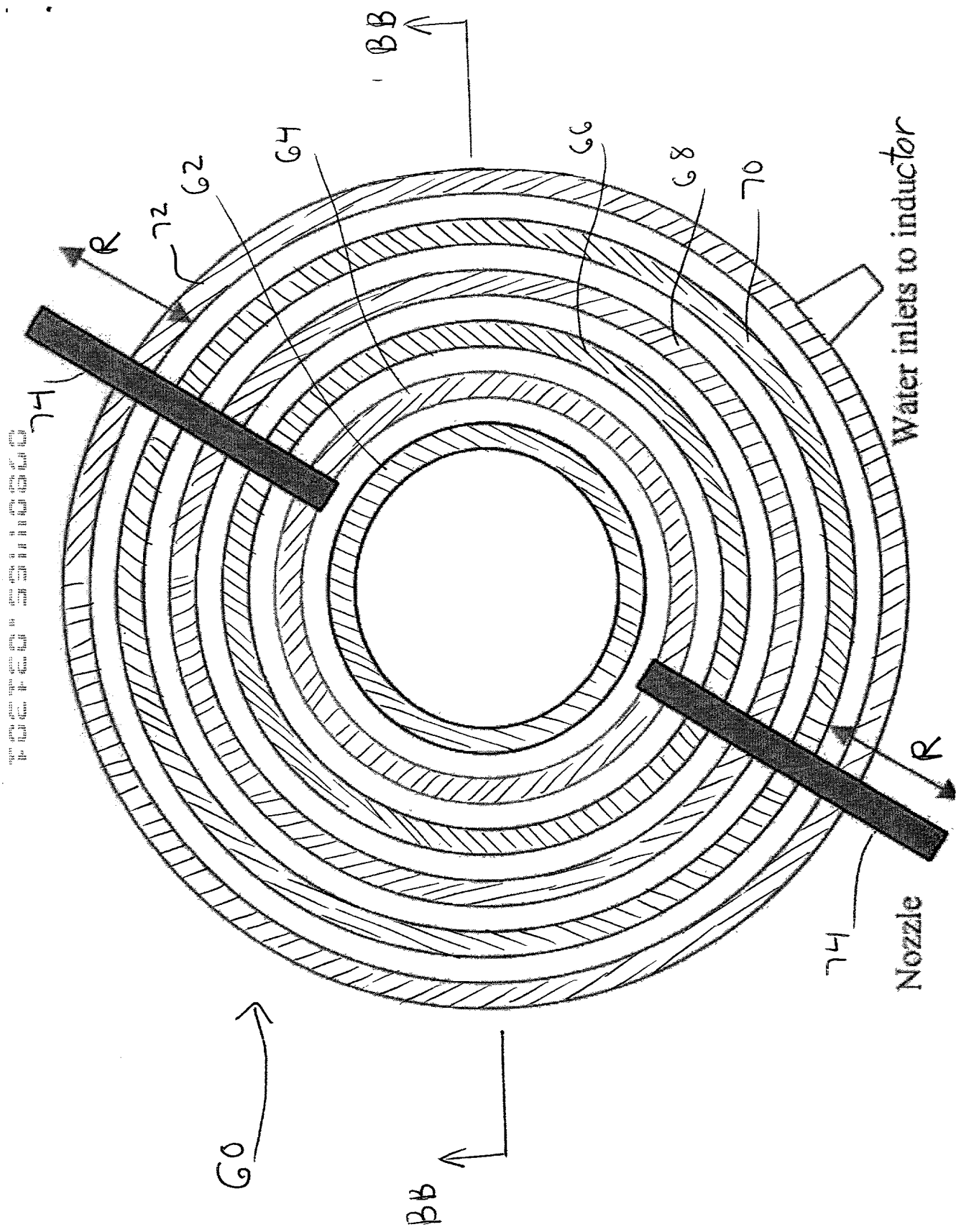


FIG. 10

FIG. 11 is a schematic diagram of a system for processing a signal.

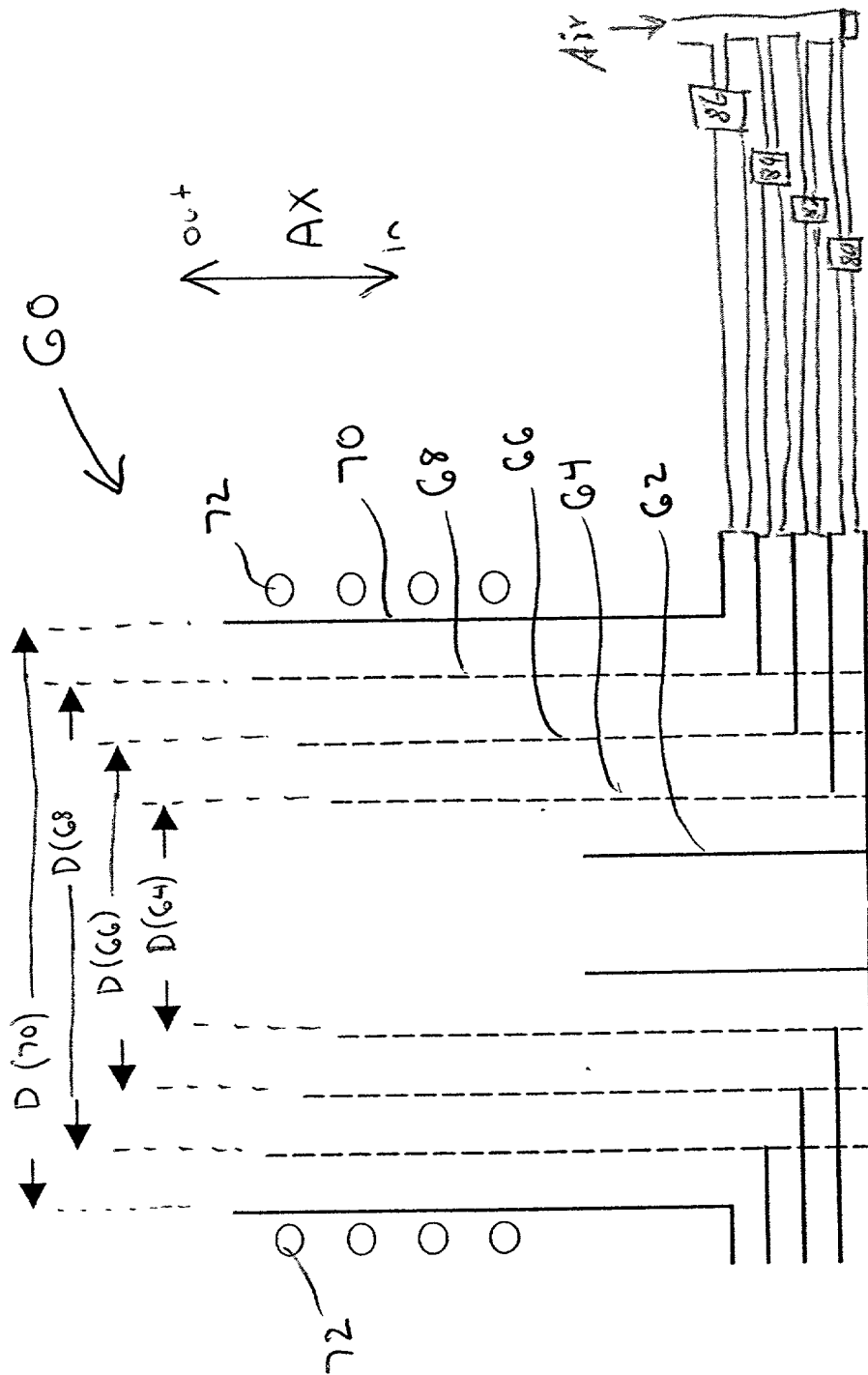


FIG. 11

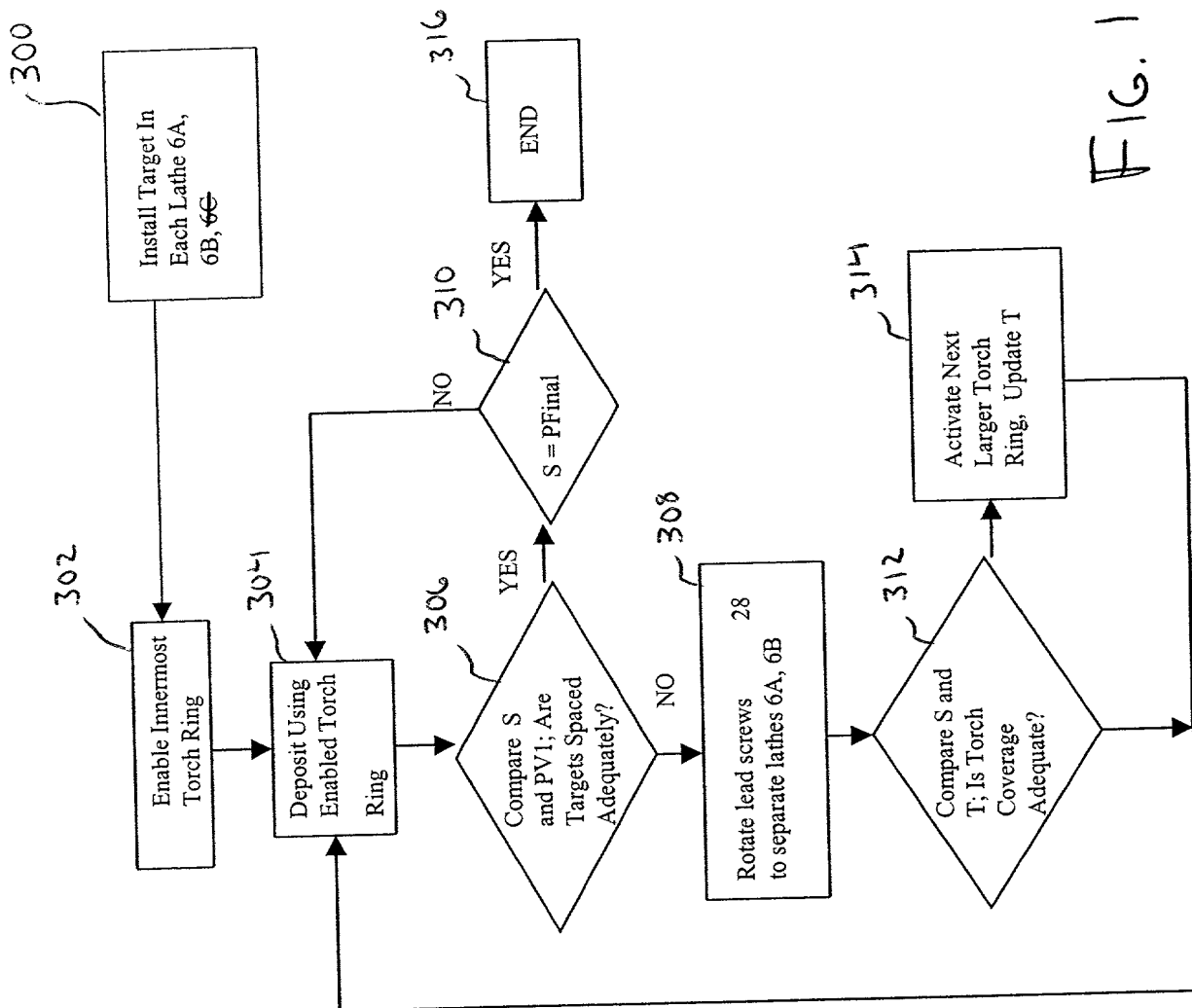


FIG. 12